



SHEET 1 OF 1 #2

INFORMATION DISCLOSURE  
CITATION

PTO-1449

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018414-133

APPLICATION NO.  
08/480,738

APPLICANT  
William K. ZURAVLEFF et al.

FILING DATE  
June 7, 1995

GROUP  
2305 Z-781

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
RP	4,833,655	05/23/89	Michnel A. Wolf et al.	365	221	06/28/85
RP	5,179,688	01/12/93	Douglas B. Brown et al.	395	425	06/30/87
RP	5,208,490	05/04/93	Jeffry D. Yetter	307	452	04/12/91
RP	5,249,297	09/28/93	Russell C. Brockmann et al.	395	725	04/29/91
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RP	5,289,403	02/22/94	Jeffry D. Yetter	365	49	07/08/91
RP	5,299,158	03/29/94	Russell W. Mason et al.	365	189.04	06/16/92
RP	5,317,204	05/31/94	Jeffry D. Yetter	307	443	05/19/92
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RP	5,343,096	08/30/94	Craig A. Heikes et al.	307	480	05/19/92

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

RP ✓	Eric DeLano et al., "A High Speed Superscaler PA-RISC Processor", IEEE, pp.116-121, 1992
RP ✓	Doug Hunt, "Advanced Performance Features of the 64-bit PA-8000", IEEE, pp.123-128, 1995
RP ✓	Jonathan Lotz et al., "A CMOS RISC CPU Designed for Sustained High Performance on Large Applications", IEEE Journal of Solid-State Circuits, vol. 25, pp.1190-1198, Oct. 1990
No dated	William Jaffe et al., "A 200MFLOP Precision Architecture Processor", Hewlett-Packard, pp.1.2.1-1.2.13

EXAMINER *RAYMOND PHAN* DATE CONSIDERED *9/9/96*

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